

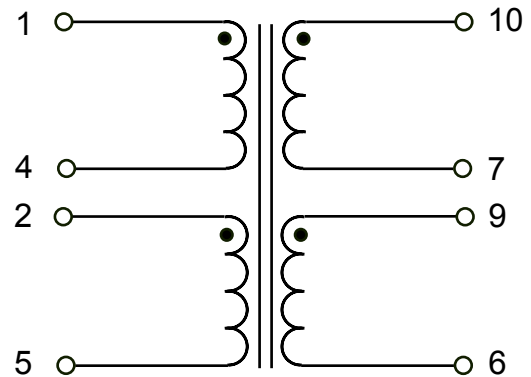
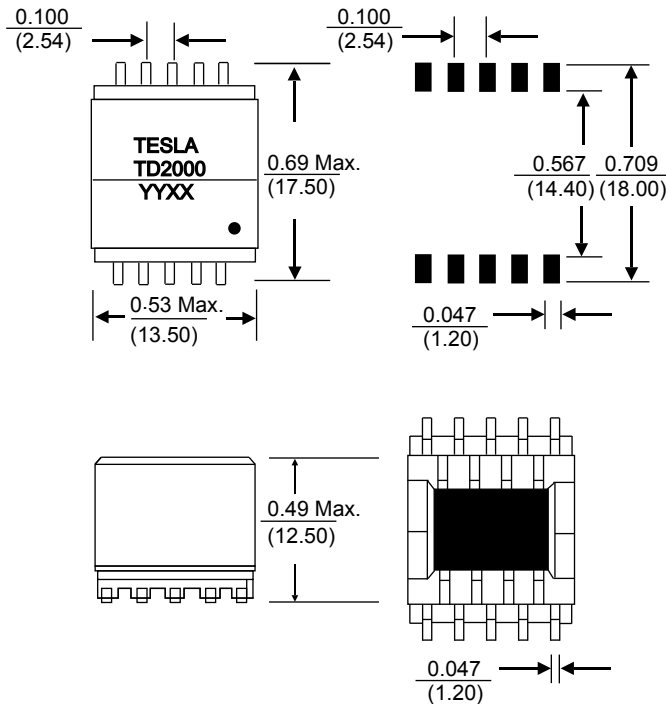
ADSL Transformer TD2000



- Optimized with Conexant chipset for CPE application
- EP 13 Transformer
- Per UL1950, EN60950 and IEC950

ELECTRICAL SPECIFICATIONS @ 25° C								
* OCL ±5%	* LL (µH Max.)	Turn Ratio (±2%)	DCR (Max.)		Hi-POT	* Cww	Longitudinal Balance (Min.)	Total Harmonic Distortion (Max.)
10 kHz 100mV	100 kHz, 100mV	10 kHz 100mV	Ω	Ω	1875Vac, 500µ A	pF	100 kHz	100 kHz
10-6 (tie 7&9)	10-6 (tie 7&9) (1&2&4&5)	(10-6): (1-5) (tie 2&4, 7&9)	1- 5 (tie 2&4)	10-6 (tie 7&9)	10-6 (tie 2&4, 7&9)	LINE : IC	dB	dB
800 µH	15	3.30:1	0.36	1.55	1 Second	65	60	-80

* OCL: Open Circuit Inductance, * LL: Leakage Inductance, * Cww: Interwinding Capacitance,



Packaging: Add -T for Tape & Reel , TD2000-T
 Dimensions are in inches/(mm) xx= ± 0.01(2.54)
 xxx= ± 0.002(0.51)